



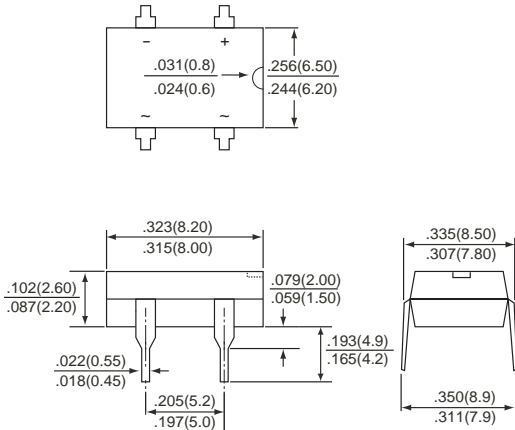
DF02 THRU DF10 GLASS PASSIVATED BRIDGE RECTIFIER

Reverse Voltage - 200 to 1000 Volts

Forward Current - 1.0 Ampere



DFM



*Dimensions in inches and (millimeters)

FEATURES

- * Glass passivated chip junctions
- * Low Forward Voltage Drop, High Current Capability
- * High Surge Current Capability
- * Designed for Surface Mount Application
- * Plastic Material-UL Recognition Flammability Classification 94V-0

MECHANICAL DATA

Case : Molded Plastic

Terminals : Tin Plated, solderable per MIL-STD-750, Method 2026

Polarity : As marked on Case

Weight : 0.38 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

<i>Ratings at 25 °C ambient temperature unless otherwise specified.</i>	SYMBOLS	DF02	DF04	DF06	DF08	DF10	UNITS
Maximum repetitive peak reverse voltage	VRRM	200	400	600	800	1000	Volts
Maximum RMS voltage	VRMS	140	280	420	560	700	Volts
Maximum DC blocking voltage	VDC	200	400	600	800	1000	Volts
Maximum average forward rectified current @ TA=40°C	I(AV)	1.0					Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	30					Amps
Maximum instantaneous forward voltage @ IF=1.0 A	VF	1.1					Volts
Maximum DC reverse current @TC=25°C at rated DC blocking voltage @TC=125°C	IR	5 500					uA
I ² t rating for fusing (t < 8.3ms)	I ² t	10.4					A ² s
Typical junction capacitance per element (NOTE 1)	CJ	25					pF
Typical thermal resistance, junction to ambient (NOTE 2)	R θJA	40					°C / W
Operating junction and storage temperature range	TJ,TSTG	-65 to +175					°C

NOTES : (1) Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.

(2) Thermal resistance from junction to ambient mounted on P.C.B. with 0.5 x 0.5" (13 x 13mm) copper pads.

RATINGS AND CHARACTERISTIC CURVES DF02 THRU DF10

FIG.1 - FORWARD CURRENT DERATING CURVE

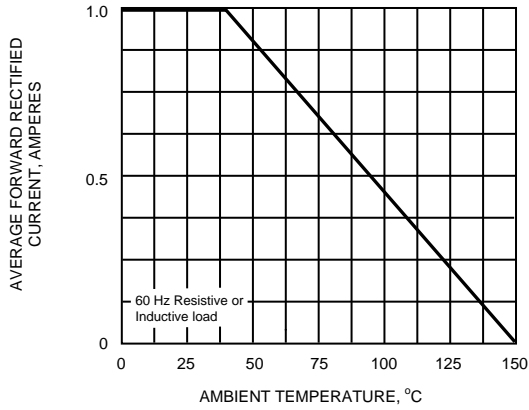


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

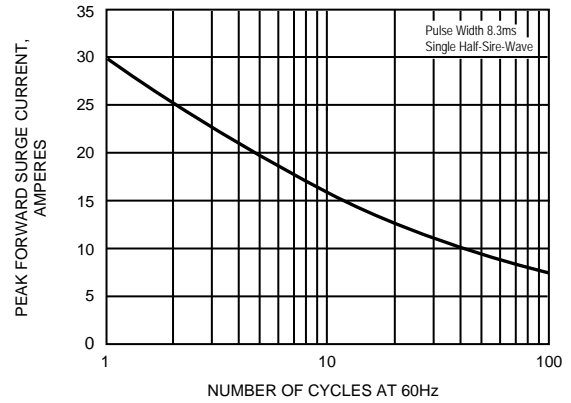


FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

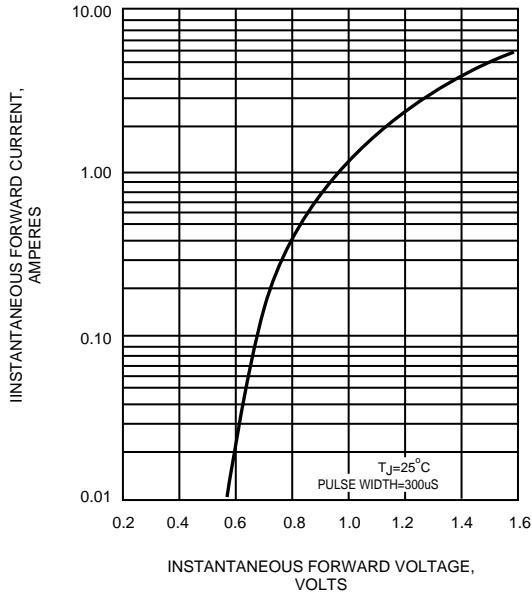


FIG.4 - TYPICAL REVERSE CHARACTERISTICS PER BRIDGE ELEMENT

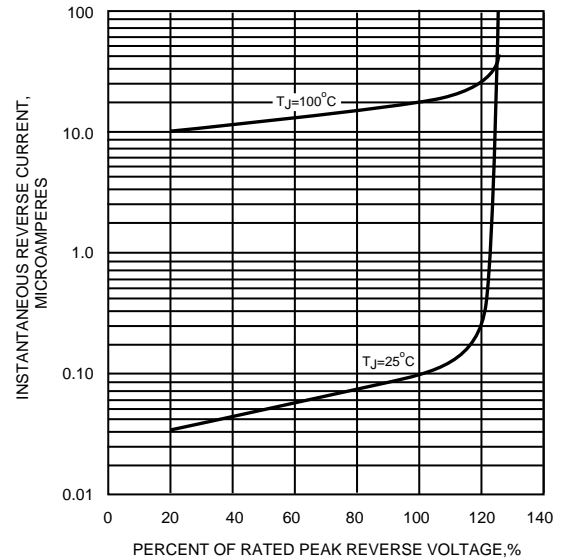


FIG.5 - TYPICAL JUNCTION CAPACITANCE

